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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 1

of

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Complete if Known

Application Number	09/880,458
Filing Date	June 12, 2001
First Named Inventor	May, Roger, et. al.
Art Unit	2182
Examiner Name	To Be Assigned
Attorney Docket Number	015114-053400US

U.S. PATENT DOCUMENTS

Examiner	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Kind Code ² (if known)			
[Signature]	0001	US-4,488,259	12-11-1984	Mercy	
	0002	US-4,617,479	10-14-1986	Hartmann et al.	
	0003	US-4,710,927	12-01-1987	Miller	
	0004	US-4,871,930	10-03-1989	Wong et al.	
	0005	US-5,241,224	08-31-1993	Pedersen et al.	
	0006	US-5,258,668	11-02-1993	Cliff et al.	
	0007	US-5,260,610	11-09-1993	Pedersen et al.	
	0008	US-5,260,611	11-09-1993	Cliff et al.	
	0009	US-5,412,260	05-02-1995	Tsui et al.	
	0010	US-5,436,575	07-25-1995	Pedersen et al.	
	0011	US-5,550,782	08-27-1996	Cliff et al.	
	0012	US-5,790,479	08-04-1998	Conn	
	0013	US-6,097,211	08-01-2000	Couts-Martin et al.	
	0014	US-6,233,205	05-15-2001	Wells et al.	

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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)				
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STATEMENT BY APPLICANT**

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Sheet

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Attorney Docket Number	015114-053400US

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
LTm	0015	AITKEN, R.C., and AGARWAL, V.K., "A Diagnosis Method Using Pseudo-Random Vectors Without Intermediate Signatures," Proc. of Int. Conf. on Computer-Aided Design (ICCAD), IEEE pp. 574-577 (1989).	
LTm	0016	Altera "APEX 20K Programmable Logic Device Family Data Sheet," May 1999.	
LTm	0017	Altera "FLEX 10K Embedded Programmable Logic Family Data Sheet," May 1999.	
LTm	0018	Altera "FLEX 8000 Programmable Logic Device Family Data Sheet," May 1999.	
LTm	0019	Altera "IEEE 1149.1 (JTAG) Boundary-Scan Testing in Altera Devices," August 1999, Application Note 39.	
LTm	0020	Altera "MAX 7000 Programmable Logic Device Family Data Sheet," May 1999.	
LTm	0021	GHOSH-DASTIDAR, J., and TOUBA, N.A., "A Rapid and Scalable Diagnosis Scheme for BIST Environments With a Large Number of Scan Chains," Proc. of IEEE VLSI Test Symposium, pp. 79-85 (2000).	
LTm	0022	GHOSH-DASTIDAR, J., DAS, D., and TOUBA, N.A., "Fault Diagnosis in Scan-Based BIST using Both Time and Space Information," Proc. of International Test. Conf., IEEE, pp. 95-102 (1999).	
LTm	0023	MC ANNEY, M.G. and SAVIR, J., "There is Information in Faulty Signatures," Proc. of International Test Conf., IEEE, pp. 630-636 (1987).	

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